

Facsimile

Arent Fox

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Attorney #: 1549
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George E. Oram, Jr.
Attorney
202.828.3455 DIRECT
202.857.6395 FAX
oram.george@arentfox.com

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U.S. Letters Patent No. 5,397,432
For: **METHOD FOR PRODUCING SEMICONDUCTOR
INTEGRATED CIRCUITS AND APPARATUS USED
IN SUCH METHOD**
By: Jun-ichi KONNO et al
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